Code Generation for Embedded Processors

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Reviews

This publication may be really worth a go through, and a lot better than other. It really is full of knowledge and wisdom Its been printed in an exceptionally easy way in fact it is simply after i finished reading this publication by which basically modified me, affect the way i really believe.
(Troy Dietrich DDS)

CODE GENERATION FOR EMBEDDED PROCESSORS

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Book Condition: New. Publisher/Verlag: Springer, Berlin | Modern electronics is driven by the explosive growth of digital communications and multi-media technology. A basic challenge is to design first-time-right complex digital systems, that meet stringent constraints on performance and power dissipation. In order to combine this growing system complexity with an increasingly short time-to-market, new system design technologies are emerging based on the paradigm of embedded programmable processors. This concept introduces modularity, flexibility and re-use in the electronic system design process. However, its success will critically depend on the availability of efficient and reliable CAD tools to design, programme and verify the functionality of embedded processors. Recently, new research efforts emerged on the edge between software compilation and hardware synthesis, to develop high-quality code generation tools for embedded processors. Code Generation for Embedded Systems provides a survey of these new developments. Although not limited to these targets, the main emphasis is on code generation for modern DSP processors. Important themes covered by the book include: the scope of general purpose versus application-specific processors, machine code quality for embedded applications, retargetability of the code generation process, machine description formalisms, and code generation methodologies. Code Generation for Embedded Systems is the essential introduction to this fast developing field of research for students, researchers, and practitioners alike. | Contributors. Preface. Part I: Requirements. 1. Code Generation for Embedded Processors: an Introduction; P. Marwedel. 2. Retargetable Code Generation: Key Issues for Successful Introduction; P. Vanoostende, et al. 3. Challenges in Code Generation for Embedded Processors; G. Araujo, et al. Part II: Retargetable Code Generation Systems. 4. FLEXWARE: A Flexible Firmware Development Environment for Embedded Systems; P.G. Paulin, et al. 5. CHESS: Retargetable Code Generation for Embedded DSP Processors; D. Lanneer, et al. 6. An ILP-Based Approach to Code Generation; T. Wilson, et al. 7. Retargetable...

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