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Using WAVES and VHDL for Effective Design and Testing A practical and useful tutorial and application guide for the Waveform and Vector Exchange Specification WAVES

By James P. Hanna

Springer. Hardcover. Book Condition: New. Hardcover. 304 pages. Dimensions: 9.4in. x 6.3in. x 1.0in. The proliferation and growth of Electronic Design Automation (EDA) has spawned many diverse and interesting technologies. One of the most prominent of these technologies is the VHSIC Hardware Description Language, or VHDL. VHDL permits designers of digital modules, components, systems, and even networks to describe their designs both structurally and behaviorally. VHDL also allows simulation of the designs in order to investigate their performance prior to actually implementing them in hardware. Having gained the ability to simulate designs once encoded in VHDL, designers were naturally confronted with the issue of testing these designs. VHDL did not explicitly address the requirement to insert particular digital waveforms, often termed test vectors or patterns, or to subsequently assess the correctness of the response from some digital entity. In a distributed design environment, or even in an isolated one where the design was subject to review or scrutiny by another organization, de-facto methods of testing and evaluating results proved faulty. The reason was a lack of standardization. When organization A designed a circuit and tested it with their self-developed test tools it had a certain behavior. When it was delivered to...

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